

Silicon NPN Power Transistors

2SC1343

DESCRIPTION

- With TO-3 package
- Wide area of safe operation

APPLICATIONS

- For audio frequency power amplifier applications

PINNING(see Fig.2)

PIN	DESCRIPTION
1	Base
2	Emitter
3	Collector

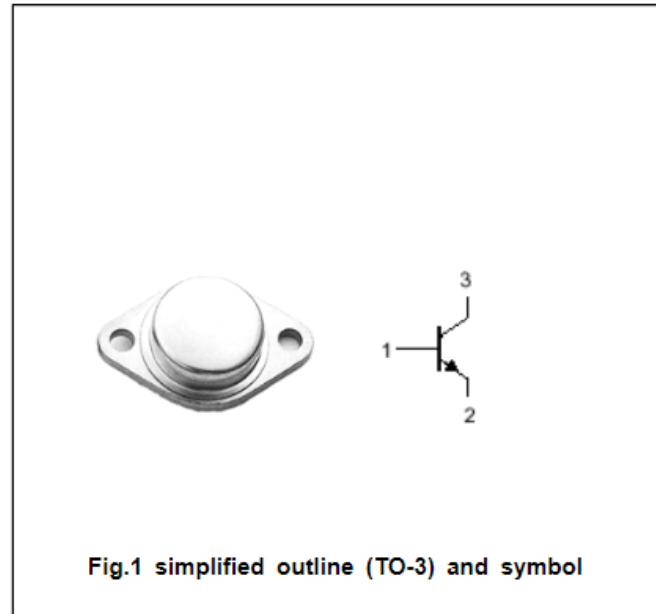


Fig.1 simplified outline (TO-3) and symbol

Absolute maximum ratings(Ta=°C)

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
V _{CB0}	Collector-base voltage	Open emitter	150	V
V _{CE0}	Collector-emitter voltage	Open base	110	V
V _{EB0}	Emitter-base voltage	Open collector	5	V
I _C	Collector current		10	A
P _C	Collector power dissipation	T _C =25°C	100	W
T _J	Junction temperature		150	°C
T _{stg}	Storage temperature		-55~150	°C

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CHARACTERISTICS

T_j=25°C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{(BR)CEO}	Collector-emitter breakdown voltage	I _C =50mA ; I _B =0	110			V
V _{(BR)EBO}	Emitter-base breakdown voltage	I _E =1mA ; I _C =0	5			V
V _{CEsat}	Collector-emitter saturation voltage	I _C =5A; I _B =0.5A			2.0	V
V _{BEsat}	Base-emitter saturation voltage	I _C =5A; I _B =0.5A			2.5	V
I _{CBO}	Collector cut-off current	V _{CB} =150V; I _E =0			0.1	mA
I _{EBO}	Emitter cut-off current	V _{EB} =5V; I _C =0			0.1	mA
h _{FE-1}	DC current gain	I _C =1A ; V _{CE} =5V	30		200	
h _{FE-2}	DC current gain	I _C =5A ; V _{CE} =5V	20			
f _T	Transition frequency	I _C =1A ; V _{CE} =5V		14		MHz

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PACKAGE OUTLINE

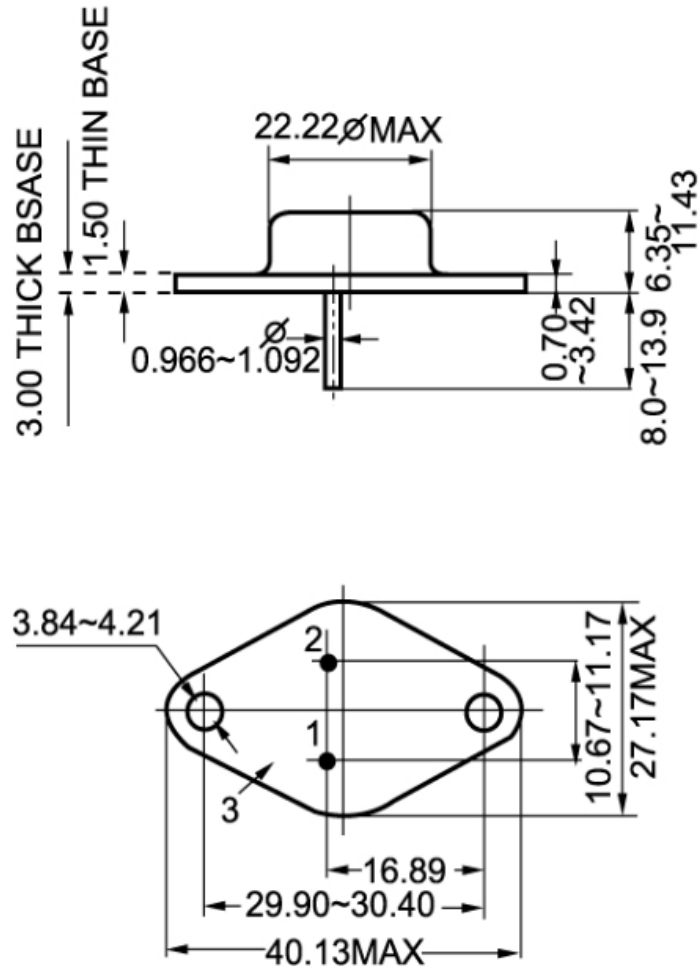


Fig.2 outline dimensions (unindicated tolerance: $\pm 0.1\text{mm}$)