

2N2696 (SILICON)

2N2927

PNP SILICON ANNULAR TRANSISTORS

... designed for use in medium-speed, non-saturated switching applications.

- High Collector-Emitter Breakdown Voltage –
BV_{CEO} = 25 Vdc @ I_C = 100 μAdc
- High Collector-Base Breakdown Voltage –
BV_{CB0} = 25 Vdc @ I_C = 100 μAdc

PNP SILICON SWITCHING TRANSISTORS



2N2696



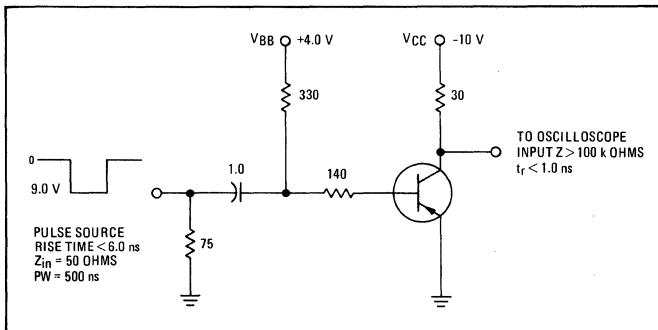
2N2927

MAXIMUM RATINGS

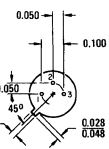
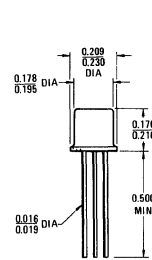
Rating	Symbol	2N2696	2N2927	Unit
Collector-Emitter Voltage	V _{CEO}	25		Vdc
Collector-Base Voltage	V _{CB}	25		Vdc
Emitter-Base Voltage	V _{EB}	4.0		Vdc
Collector Current – Continuous	I _C	500		mAdc
Total Device Dissipation @ T _A = 25°C Derate above 25°C	P _D	0.36 2.06	0.8 4.56	Watts mW/°C
Total Device Dissipation @ T _C = 25°C Derate above 25°C	P _D	1.2 6.85	3.0 17.1	Watts mW/°C
Operating and Storage Junction Temperature Range	T _J , T _{stg}	-65 to +200		°C

*Indicates JEDEC Registered Data.

FIGURE 1 – TURN-ON AND TURN-OFF TIME TEST CIRCUIT



2N2696

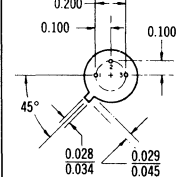
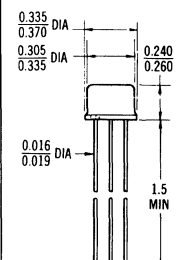


Pin 1. Emitter
2. Base
3. Collector

Collector
connected to case

CASE 22 (1)
(TO-18)

2N2927



Pin 1. Emitter
2. Base
3. Collector

Collector
connected to case

CASE 31 (1)
(TO-5)

2N2696, 2N2927 (continued)

*ELECTRICAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$ unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit	
OFF CHARACTERISTICS					
Collector-Emitter Breakdown Voltage** ($I_C = 100 \mu\text{A dc}$, $I_B = 0$) ($I_C = 10 \text{ mA dc}$, $I_B = 0$)	2N2927 2N2696	BV_{CEO}	25 25	— —	Vdc
Collector-Base Breakdown Voltage ($I_C = 100 \mu\text{A dc}$, $I_E = 0$)		BV_{CBO}	25	—	Vdc
Collector Cutoff Current ($V_{CE} = 15 \text{ V dc}$, $V_{BE} = 0$)	2N2927	I_{CES}	—	25	nA dc
Collector Cutoff Current ($V_{CB} = 10 \text{ V dc}$, $I_E = 0$) ($V_{CB} = 10 \text{ V dc}$, $I_E = 0$, $T_A = 150^\circ\text{C}$) ($V_{CB} = 15 \text{ V dc}$, $I_E = 0$, $T_A = 125^\circ\text{C}$)	2N2927	I_{CBO}	— — —	0.025 5.0 5.0	$\mu\text{A dc}$ $\mu\text{A dc}$ $\mu\text{A dc}$
Emitter Cutoff Current ($V_{EB} = 4.0 \text{ V dc}$, $I_C = 0$)		I_{EBO}	—	100	$\mu\text{A dc}$
Base Current ($V_{CE} = 15 \text{ V dc}$, $V_{BE} = 0$)	2N2927	I_B	—	25	nA dc
ON CHARACTERISTICS					
DC Current Gain ($I_C = 50 \text{ mA dc}$, $V_{CE} = 1.0 \text{ V dc}$) ($I_C = 50 \text{ mA dc}$, $V_{CE} = 1.0 \text{ V dc}$, $T_A = -55^\circ\text{C}$) ($I_C = 300 \text{ mA dc}$, $V_{CE} = 2.0 \text{ V dc}$)(1)		h_{FE}	30 12 20	130 — —	—
Collector-Emitter Saturation Voltage ($I_C = 50 \text{ mA dc}$, $I_B = 2.5 \text{ mA dc}$) ($I_C = 300 \text{ mA dc}$, $I_B = 30 \text{ mA dc}$)		$V_{CE(sat)}$	— —	0.25 1.0	Vdc
Base-Emitter Saturation Voltage ($I_C = 50 \text{ mA dc}$, $I_B = 2.5 \text{ mA dc}$) ($I_C = 300 \text{ mA dc}$, $I_B = 30 \text{ mA dc}$)		$V_{BE(sat)}$	— —	1.1 2.0	Vdc
Base-Emitter On Voltage ($I_C = 50 \text{ mA dc}$, $V_{CE} = 1.0 \text{ V dc}$)	2N2927	$V_{BE(on)}$	—	1.0	Vdc
SMALL-SIGNAL CHARACTERISTICS					
Output Capacitance ($V_{CB} = 10 \text{ V dc}$, $I_E = 0$, $f = 140 \text{ kHz}$)		C_{ob}	—	20	pF
Input Impedance ($I_C = 10 \text{ mA dc}$, $V_{CE} = 10 \text{ V dc}$, $f = 1.0 \text{ kHz}$)		h_{ie}	—	1500	ohms
Voltage Feedback Ratio ($I_C = 10 \text{ mA dc}$, $V_{CE} = 10 \text{ V dc}$, $f = 1.0 \text{ kHz}$)		h_{re}	—	26	$\times 10^{-4}$
Small-Signal Current Gain ($I_C = 10 \text{ mA dc}$, $V_{CE} = 10 \text{ V dc}$, $f = 1.0 \text{ kHz}$) ($I_C = 50 \text{ mA dc}$, $V_{CE} = 3.0 \text{ V dc}$, $f = 100 \text{ MHz}$)		h_{fe}	25 1.0	180 —	—
Output Admittance ($I_C = 10 \text{ mA dc}$, $V_{CE} = 10 \text{ V dc}$, $f = 1.0 \text{ kHz}$)		h_{oe}	—	1200	μmhos
SWITCHING CHARACTERISTICS					
Turn-On Time ($V_{CC} = 10 \text{ V dc}$, $I_C \approx 300 \text{ mA dc}$, $I_{B1} \approx 30 \text{ mA dc}$) (See Figure 1)		t_{on}	—	75	ns
Turn-Off Time ($V_{CC} = 10 \text{ V dc}$, $I_C \approx 300 \text{ mA dc}$, $I_{B1} = I_{B2} \approx 30 \text{ mA dc}$) (See Figure 1)		t_{off}	—	170	ns

*Indicates JEDEC Registered Data.

**Motorola Guarantees this data in addition to JEDEC Registered Data.

(1) Pulse Test: Pulse Width = 300 μs , Duty Cycle = 1.0%.