TOSHIBA Power MOS FET Module Silicon N Channel MOS Type (L²-π-MOSV 4 in 1)

MP4209

High Power, High Speed Switching Applications For Printer Head Pin Driver and Pulse Motor Driver For Solenoid Driver

- 4 V gate drive available
- Small package by full molding (SIP 10 pin)
- High drain power dissipation (4 devices operation) : $P_T = 4 \text{ W} (T_a = 25^{\circ}\text{C})$
- Low drain-source ON resistance: RDS (ON) = 0.28Ω (typ.)
- High forward transfer admittance: $|Y_{fs}| = 3.5 \text{ S}$ (typ.)
- Low leakage current: I_{GSS} = ±10 μA (max) (V_{GS} = ±16 V) I_{DSS} = 100 μA (max) (V_{DS} = 100 V)
- Enhancement-mode: V_{th} = 0.8 to 2.0 V (V_{DS} = 10 V, I_D = 1 mA)

Maximum Ratings (Ta = 25°C)

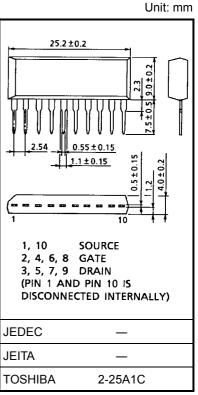
Characteristic	Symbol	Rating	Unit		
Drain-source voltage		V _{DSS}	100	V	
Drain-gate voltage (R _{GS}	V _{DGR}	100	V		
Gate-source voltage	V _{GSS}	±20	V		
Drain current	DC	ID	3	А	
	Pulse	I _{DP}	12	A	
Drain power dissipation (1 device operation, Ta = 25°C)		PD	2.0	w	
Drain power dissipation (4 devices operation, Ta	= 25°C)	P _{DT}	4.0	W	
Single pulse avalanche e	energy (Note 1)	E _{AS}	140	mJ	
Avalanche current		I _{AR}	3	А	
Repetitive avalanche energy (Note 2)	1 device operation	E _{AR}	0.2	mJ	
	4 devices operation	E _{ART}	0.4	IIIJ	
Channel temperature		T _{ch}	150	°C	
Storage temperature ran	ge	T _{stg}	-55 to 150	°C	

Note 1: Avalanche energy (single pulse) applied condition V_{DD} = 50 V, starting T_{ch} = 25°C, L = 20 mH, R_G = 25 Ω , I_{AR} = 3 A

Note 2: Repetitive rating; pulse width limited by maximum channel temperature.

This transistor is an electrostatic sensitive device. Please handle with caution.

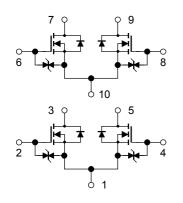




Weight: 2.1 g (typ.)

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Array Configuration



Thermal Characteristics

Characteristics	Symbol	Max	Unit	
Thermal resistance of channel to ambient	f channel to ΣR _{th (ch-a)}		°C/W	
(4 devices operation, Ta = 25°C)	. ,			
Maximum lead temperature for soldering purposes	TL	260	°C	
(3.2 mm from case for t = 10 s)	_			

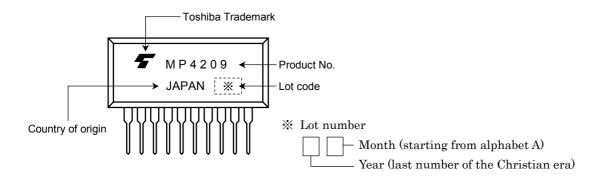
Electrical Characteristics (Ta = 25°C)

Chara	acteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cur	rent	I _{GSS}	V_{GS} = ±16 V, V_{DS} = 0 V	_	-	±10	μA
Drain cut-off curr	Drain cut-off current ID		V _{DS} = 100 V, V _{GS} = 0 V	_	_	100	μA
Drain-source bre	akdown voltage	V (BR) DSS	I _D = 10 mA, V _{GS} = 0 V	100	_	_	V
Gate threshold ve	oltage	V _{th}	V _{DS} = 10 V, I _D = 1 mA	0.8	_	2.0	V
Drain-source ON resistance		Pro (out)	V _{GS} = 4 V, I _D = 2 A	_	0.36	0.45	Ω
		R _{DS (ON)}	V _{GS} = 10 V, I _D = 2 A	—	0.28	0.35	
Forward transfer	admittance	Y _{fs}	V _{DS} = 10 V, I _D = 2 A	1.5	3.5	_	S
Input capacitance		C _{iss}	- V _{DS} = 10 V, V _{GS} = 0 V - f = 1 MHz	_	280	-	pF
Reverse transfer capacitance		C _{rss}		_	50	_	pF
Output capacitance		C _{oss}		_	105		pF
Switching time	Rise time	tr	$V_{GS} = 2 A$ $V_{GS} = 0 V$ $V_{IN}: t_r, t_f < 5 ns, duty \le 1\%, t_w = 10 \ \mu s$	_	20	_	
	Turn-on time	t _{on}		_	50		20
	Fall time	t _f		_	40		ns
	Turn-off time	t _{off}		_	170	_	
Total gate charge (gate-source plus gate-drain)		Qg	V _{DD} ≈ 80 V, V _{GS} = 10 V	_	13.5	_	nC
Gate-source charge		Q _{gs}	I _D = 3 A	—	8.5	—	nC
Gate-drain ("miller") charge		Q _{gd}		_	5	_	nC

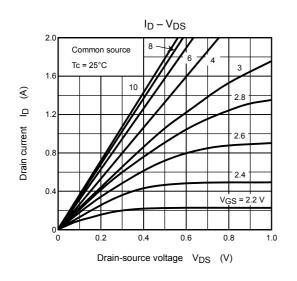
Source-Drain Diode Ratings and Characteristics (Ta = 25°C)

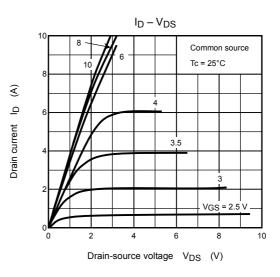
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current	I _{DR}	—	_	_	3	А
Pulse drain reverse current	I _{DRP}	—			12	А
Diode forward voltage	V _{DSF}	I _{DR} = 3 A, V _{GS} = 0 V	-	-	-1.5	V
Reverse recovery time	t _{rr}	I _{DR} = 3 A, V _{GS} = 0 V	_	100	_	ns
Reverse recovery charge	Q _{rr}	dI _{DR} /dt = 50 A/µs	_	0.2	—	μC

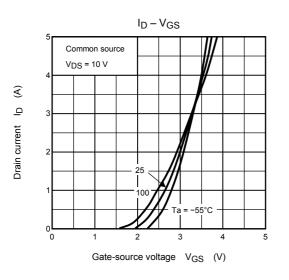
Marking

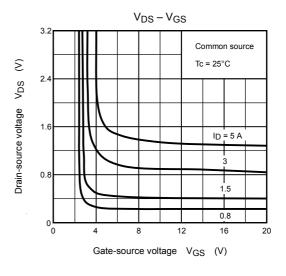


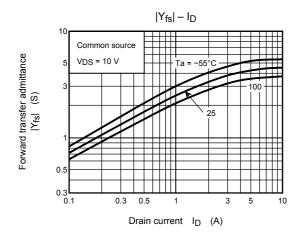
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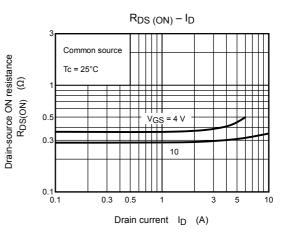




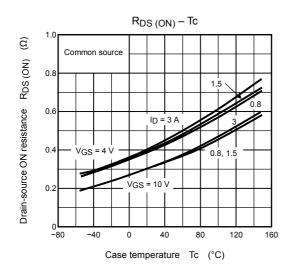


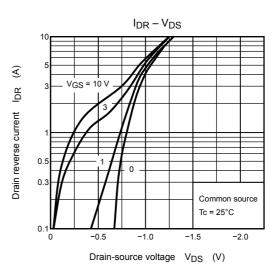


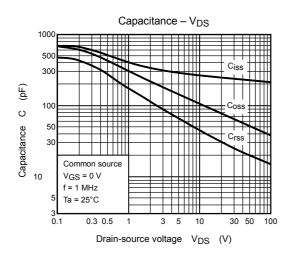


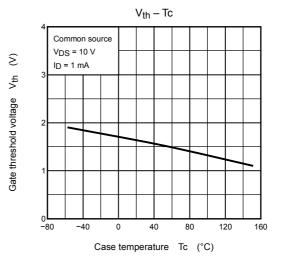


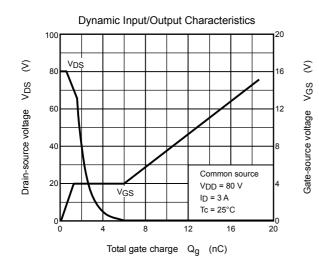
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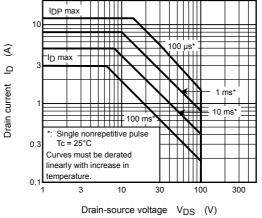


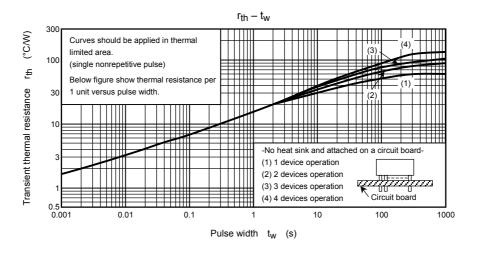


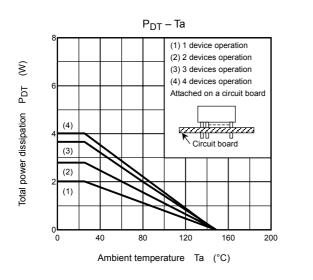


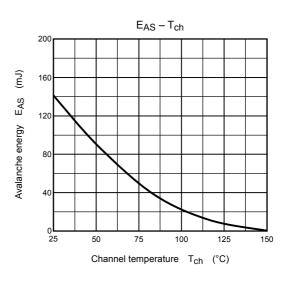


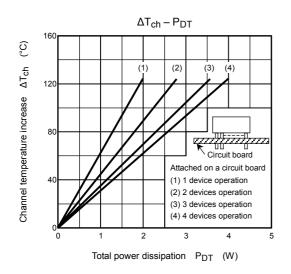
Safe Operating Area

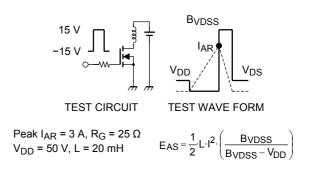












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